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PG-EE-2013

SUBJECT: M.C.A.-LE

В		Sr. No. 10034
Time : 11/4 Hours	Max. Marks: 100	Total Questions: 100
Roll No. (in figures)	(in words)	
Name	Father's Name	
Mother's Name	Date of Examination _	
(Signature of the Candidate)		(Signature of the Invigilator)
DECEMBER OF STREET		

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- 1. All questions are compulsory and carry equal marks.
- 2. The candidates must return the question booklet as well as OMR Answer-Sheet to the Invigilator concerned before leaving the Examination Hall, failing which a case of use of unfair-means/misbehaviour will be registered against him/her, in addition to lodging of an FIR with the police. Further the answer-sheet of such a candidate will not be evaluated.
- 3. In case there is any discrepancy in any question(s) in the Question Booklet, the same may be brought to the notice of the Controller of Examinations in writing within two hours after the test is over. No such complaint(s) will be entertained thereafter.
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- 5. Use only black or blue ball point pen of good quality in the OMR Answer-Sheet.
- 6. There will be no negative marking. Each correct answer will be awarded one full mark. Cutting, erasing, overwriting and more than one answer in OMR Answer-Sheet will be treated as incorrect answer.
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1.	"%s" is used for specifying: (1) Signed Variable (3) String	(2) Short Variable (4) All of the above
2.	free () is a: (1) Memory Allocation Function (3) Memory De-Allocation Function	(2) Recursive Function (4) None
3.	Cgets () and Cputs () are defined in: (1) stdio,h (2) conio.h	(3) math.h (4) All of the above
4.	To get a character from unformatted I/C	Data Files, we have to use :
	(1) scanf () (2) getche ()	(3) getchar() (4) getc()
5.	<pre>Write the Output for : main () { int i = 5, j = 6; printf ("% d", 0 && (i = 10)); } (1) 0 (2) 1</pre>	(3) 10 (4) None
6.	<pre>Write the output of : main () { int n 1 = 4; printf ("%d", size of (n1)); }</pre>	To any district and probabilities will be required and the region of
		(2) 4 (4) 8
7.	(a) E 1:	be overloaded ? (2) Arrow operator (4) Assignment operator
8.	How do we declare an interface class? (1) By declaring the class as interface with (2) By making all the methods abstract up (3) By making all the methods pure virtue (4) It is not possible to create interface of	using the keyword abstract in class

9.	What is the right way to declare a cop (1) X (const X* arg) (3) X (X arg)	y constructor of a class if the name of class is X? (2) X (const X & arg) (4) X (X* arg)
10.	Which statement is most appropriate (1) Type less language (3) Dynamically typed language	for C++ language? (2) Statically types language (4) Both (2) and (3)
11.	What is the worst-case time for heaps (1) O (log n) (2) O (n)	sort to sort an array of n elements? (3) O (n log n) (4) O (n²)
12.	In AVL tree the balance factor of ever (1) 0 (2) 0 or 1	ry node should be: (3) -1,0 or 1 (4) -1 or 1
13.	The elements of linked list are stored (1) Successive memory locations (3) Alternate memory locations	in: (2) Random memory locations (4) None of the above
14.	Queue data structure is based on the (1) Last come first serve (3) Both (1) & (2) above	principle of: (2) First come first serve (4) None of the above
15.	Which of the following is a non-linear (1) Tree (3) Linked list	(2) Array (4) None of the above
16.	To implement recursion, which of the (1) Stack (3) Tree	e following data structure is required? (2) Queue (4) None of the above
17.	Array passed as an argument to a full (1) Address of the array (2) Values of the first elements of the (3) Address of the first element of the (4) Number of element of the array	e array ne array
18.	Time taken for addition of element i (1) O (l) (3) O (log n)	n queue is: (2) O (n) (4) None of these options

19.	The memory address of the first element	of	an array is called :
	(1) Floor address	(2)	Foundation address
	(3) First address	(4)	Base address
20.	The memory address of fifth element of	an a	array can be calculated by the formula:
	(1) LOC (Array [5]) = Base (Array) + w words per memory cell for the array		-lower bound), where w is the number of
	(2) LOC (Array [5]) = Base (Array [5]) of words per memory cell for the arr		(5-lower bound), where w is the number
	(3) LOC (Array [5]) = Base (Array [4]) - of words per memory cell for the arr		(5-Upper bound), where w is the number
	(4) None of the above		
21.	Which of the following is not viewed as process?	saj	orimary mover in improving the software
	(1) Increased effectiveness(3) Improved Staff Satisfaction		Better product quality Tighter managerial control
22.	Symptoms of the software crisis would i	ncli	ıde:
	(1) Software delivered behind schedule(3) Unreliable	(2)	
23.	Which of the following projects would paradigm for software development?	be	a good one for adopting the prototyping
	(1) Accounting system (3) Automobile cruise control		Spreadsheet Algebra tutor
24.	Views of quality software would not inc (1) Optimizing price and performance (2) Minimizing the execution errors (3) Conformance to specification (4) Establishing valid requirements	lud	
25.	Software configuration activities would	not	include:
	(1) Identify change		
	(2) Control change		Separate September 200 Sep
	(3) Ensure improper implementation of	cha	ange

(4) Report change to interested parties

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	26.	In planning a software project one work (1) Find ways to produce results using (2) Pad the schedule to accommodate (3) Overestimate the budget (4) Structure the team to prevent admit	g limit errors	red resources
	27.	A systematic approach to software decycle models, is useful in: (1) Helping us understand the nature (2) Convincing the customer that we lead to be software engineering. (3) Filling texts on software engineering. (4) Managing the various activities necessity.	of the know ng	what we are doing
	28.	A process view in software engineering	g wou	ald consider which of the following?
		(1) Product performance	(2)	Staffing
		(3) Functionality	(4)	Reliability
	29.	Software measurement is useful to:		
		(1) Indicate quality of the product	(2)	Track progress
		(3) Assess productivity	(4)	All of the above
	30.	Which of the following is not a 'concern (1) Money (3) Product quality	(2)	g the management of a software project? Time Product quantity
	31.	The collection of communication lines	and r	outers is called:
		(1) LAN	(2)	MAN
		(3) WAN	(4)	Communication Subnet
	32.	In the IEEE standards 802.5 standard i	is also	called
		(1) Ethernet	35	Token Bus
		(3) Wireless Token Area Network	(4)	Token Ring
	33.	have a single communication network.		innel that is shared by all the users on th
		(1) Point-to-Point	(2)	Broadcast network
		(3) Protocol		Annual testings the
	34.	Error detection at the data link level is		eved by
		(1) Bit stuffing	(2)	
		(3) Cyclic Redundancy codes	(4)	Equalization

35.	(1) Resistance to data theft (3) Low noise level	(2)	Fast data transmission rate All of the above
36.	Which of the following is required to co (1) Communication software (2) Protocol (3) Communication hardware (4) All of the above including access to	mm	unicate between two computers? description description
37.	Which of the following types of channel (1) Wide band channel (3) Narrow band channel	(2)	Voice band channel All of the above
38.	A protocol is a set of rules governing a to (1) between peers (3) between modems	(2)	sequence of events that must take place : between an interface across an interface
39.	Which of the following transmission individual device? (1) Computer bus (3) Voice and mode	(2)	ems provide the highest data rate to in Telephone lines Lease lines
40.	Communication circuits that transmit dare operating in: (1) a simplex mode (3) a full duplex mode	(2)	in both directions but not at the same time a half duplex mode an asynchronous mode
	If a, b, c are different and: $\begin{vmatrix} 0 & x-a & x-b \\ x+a & 0 & x-c \\ x+b & x+c & 0 \end{vmatrix} = 0, \text{ then } x \text{ is equal}$	to	Thenivas
42.		(3)	
	(1) 3	(2)	4

(4) 5

(3) 2

43. If
$$A = \begin{bmatrix} 3 & -3 & 4 \\ 2 & -3 & 4 \\ 0 & -1 & 1 \end{bmatrix}$$
, then $A^{-1} = \begin{bmatrix} 3 & -3 & 4 \\ 2 & -3 & 4 \\ 0 & -1 & 1 \end{bmatrix}$

- (1) A (2) A^2 (3) A^3
- **44.** If $A_{\alpha} = \begin{bmatrix} \cos \alpha & \sin \alpha \\ -\sin \alpha & \cos \alpha \end{bmatrix}$, then $A_{\alpha} A_{\beta}$ is equal to:

- (1) $A_{\alpha+\beta}$ (2) $A_{\alpha\beta}$ (3) $A_{\alpha-\beta}$ (4) none of these
- If A and B are symmetric matrices, then AB BA is a:

 - (1) Symmetric matrix (2) Skew symmetric matrix
 - (3) Diagonal matrix

- (4) Null matrix
- The inverse of a skew symmetric matrix of odd order is:
 - (1) a symmetric matrix
- (2) a skew symmetric matrix

- (3) diagonal matrix
- (4) does not exist
- The value of λ for which the system of equations :

$$2x - y - 2x = 2$$

$$x - 2y + z = -4$$

$$x + y + \lambda z = 4$$

has no solution, is

- (1) -3
- (2) 3 (3) 2
- **48.** If $A = \begin{bmatrix} \alpha & 0 \\ 1 & 1 \end{bmatrix}$ and $B = \begin{bmatrix} 1 & 0 \\ 5 & 1 \end{bmatrix}$, then the value of α for which $A^2 = B$, is:
 - (1) 1
- (2) -1
- (3) 4
- (4) no real values
- a 1 1 **49.** If the value of the determinant $\begin{vmatrix} 1 & b & 1 \end{vmatrix}$ is positive, then: 1 1 c
 - (1) abc > 1
- (2) abc > -8 (3) abc < -8 (4) abc > -2
- The value of $\begin{vmatrix} a & a^2 bc & 1 \\ b & b^2 ca & 1 \\ c & c^2 ab & 1 \end{vmatrix}$ is
- (2) -1 (3) 0
- (4) -abc

51.	1. Among the logic families, the family which can greater than 100 MHz in a 4 bit synchronous counter	be used at very high frequency is:
	(1) TTLAS (2) CMOS (3) ECL	(4) TTLLS
52.	 2. An AND gate will function as OR if: (1) all the inputs to the gates are "1" (2) all the inputs are '0' (3) either of the inputs is "1" (4) all the inputs and outputs are complemented 	entraces a declarate (F) respective a declarate (F) respective adoption (F) respective adoption (F)
53.	3. An OR gate has 6 inputs. The number of input words	s in its truth table are:
	(1) 6 (2) 32 (3) 64	(4) 128
54.		
		used to make any gate
	(3) consume least electronic power (4) provide	maximum density in a chip
55.	5. In case of OR gate, no matter what the number of inp	outs, a
	(1) 1 at any input causes the output to be at logic 1	
	(2) 1 at any input causes the output to be at logic 0	28 - 167 i marriage (A. 22)
	(3) 0 any input causes the output to be at logic 0	
*	(4) 0 at any input causes the output to be at logic 1	
56.	6. The most common addressing techniques employed	by a CPU is:
	(1) immediate (2) direct	
	(3) indirect (4) all of the	e above
57.	7. Pipeline implement :	Ballering 18
	(1) fetch instruction and decode instruction	to the disease of the second second
		brown with the minutes of \$2.
	(3) execute instruction	and Additional I
	(4) all of the above	a COMPANIE NA SE
58.	8. Which of the following code is used in present day of corporation?	computing was developed by IBM
	에 보면 보면 보다는 이 사는 사용 전에 가장 보다는 것이 되었다. 그는 사용 전에 되었다면 보다는 것이 되었다면 보다는 것이 되었다면 그렇지만 보다는 것이 없다면 보다는 것이 없다면 보다. 그렇지 때문에 보다 되었다면 보다는 것이 없다면 보다는 것이 없다면 보다는 것이 없다면 보다면 보다면 보다면 보다면 보다면 보다면 보다면 보다면 보다면 보	th Code
	(3) Baudot code (4) EBCDI	C code

59.	instructions stored in/on the:	ddress of the instruction following the CALI
#	(1) Stack pointer (3) Program counter	
60.	A microprogram written as string of (1) Symbolic microinstruction (3) Symbolic microprogram	0's and 1's is a : (2) Binary microinstruction (4) Binary microprogram
61.	is used to build 'intuitive' (1) SQL (2) Xbase	queries. (3) QBE (4) XML
62.	To print information from a table, the (1) form (2) report	tool is the best choice :
63.	To store your picture in a database re (1) BLOB (2) text	
64.	The is used to access one re-	cord at a time : (3) form (4) report
65.	A transparent DBMS: (1) Can not hide sensitive information (2) Keep its logical structure hidden (3) Keeps its physical structure hidden (4) Both (2) and (3)	from users
66.	An unnormalized relation contains va (1) Atomic (3) Classified	llues : (2) Non-Atomic (4) None of these
67.	A relation scheme is said to be in attribute of the relation are atomic. (1) Unnormalized and are atomic. (3) Boyce CODD	form if the values in the domain of each (2) First Normal (4) None of these
68.	A second normal form does not per attribute and the relation key.	mitdependency between a non prime
	(1) Partial	(2) Multi

69.	A relation scheme is inif it is in the functionally dependent on the relation		IF and if all non prime attributes are fully
	(1) First Normal Form		Second Normal Form
	(3) Boyce CODD Normal Form		Fourth Normal Form
70.	In a Third Normal Form relation, ever dependent on the every candidate key.		attribute is non-transitively and fully
	(1) Prime (2) Non prime	(3)	Unique (4) None of these
71.	page replacement algorithm s		
	(1) LRU	(2)	MRU
	(3) FIFO	(4)	LIFO
72.	is a high speed cache used to part of paged virtual memory.	o ho	ld recently referenced page table entries a
	(1) Translation lookaside buffer	(2)	Inverse page table
	(3) Segmented page table	(4)	All of the above .
73.	does the job of allocating a pro	cess	to the processor.
	(1) Long term scheduler		Short term scheduler
	(3) Medium term scheduler	(4)	Dispatcher
74.	to provide reasonably good response	time luling TN)	aaring systems, the primary requirement is and in general, to share system resources g algorithm that is most popularly applied Scheduling
75.	In the multi-programming environm number of process.	ent,	the main memory consisting of
1	(1) Greater than 100		Only one
	(3) Greater than 50	(4)	More than one
76.	In a multithreaded environment		· AARIBAARIBAARIBAARIBAARIBAARIBAARIBAARI
	(1) Each thread is allocated with new	mem	ory from main memory
	(2) Main thread terminates after the te		
	(3) Every process can have only one th		
	(4) None of the above		
CF	5-2013 (M.C.A LE)/(B)		P. T. O.

77.		
	(1) Multiprogramming implies multitasking	ng
	(2) Multi-user does not imply multiproces	sing
	(3) Multitasking does not imply multiproduced	ressing
	(4) Multithreading implies multi-user	successive of the transfer of the
78.		ds, impose a total ordering of all resource uests resources in an increasing order of addition of deadlock.
) Hold and wait
) No preemption
79.	. In themethod of data transfer eliminated during data transfer.	r, the participation of the processor is
	(1) Buffering (2) Caching
	(3) Direct Memory Access (4)) Indirect Memory Access
80.	. A thread is aprocess.	
) Multiprocess
		Light weight
81.	. A Pseudo code is a	
	(1) Program (2) Algorithm (3) Can't say (4) None
82.	 SCSI is: (1) Small Computer System Interface (2) Small Computer System Interchange (3) Simple Computer Secondary Information (4) Simple Computer Secondary Interface 	
83.	BIOS stands for :	
00.		Begin Input Output System
	(3) Basic Instruction Output System (4)) Base Instruction Output System
84.	. What type of computer chips are said to be	volatile?
	(1) RAM chips (2) ROM chips
	(3) All of the above (4)	None of the above
85.	. Which of the following memories has the s	hortest access times?
) Magnetic core memory
	(3) RAM (4) PROM

86.	According to Niklaus Wirth, a computer		
	(1) Algorithm and data		Algorithm only
	(3) Data only	(4)	None of the above
87.	Graphical representation of control flow	in a	program can be depicted through:
	(1) Gantt chart	(2)	Entity-relationship diagram
	(3) Flow chart	(4)	None of the above
88.	In flow chart, diamond box is used for: (1) Decision/conditional checking (2) Data input/output (3) Calculation and data manipulation		
	(4) As connector		les (se almes brases pressabilités (R.V.)
89.	Which Loop is called as Entry Control L		
	(1) Do While		WhileDo
	(3) Both	(4)	None
90.	strlwr () is used for:		BOOK SEES AND COMPLETE OF THE
	(1) Converting string into Integer		Converting string into Lower Case
	(3) Converting string into Upper Case		Converting string into Octal form
91.	What type of information should you av		
	(1) Links to sites of interest		Private personal information Graphical
	(3) Work and academic experience		
92.	Which of the following web elements website? (1) the web audience (2) the operating environment of your (3) the operating system of your visitor	isp	uld you know about before building your
	(4) each consideration should determin	ne yo	our web design choices
93.	What is the language of the web?		
	(1) Basic	(2)	C++
	(3) MS Visual Basic	(4)	HTML

94. What does an HTML tag do?

PG-EE-2013 (M.C.A. - LE)/(B)

	(2) It flides programming instruc	
	(3) it determines the organization	nal structure of your website
	(4) it connects your website to ar	operating environment
95		sections. What are the tags called that create the
	(1) Structure tags	(2) html tags
	(3) heading tags	(4) body tags
96.	What should be the first and last p (1)	

(1) it specifies formatting and layout instructions for your web page