Total No. of Printed Pages: 13

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PG-EE-2013

SUBJECT: M.C.A.-LE

| A | | Sr. No. 10045 |
|--------------------------------------|-----------------------------|--------------------------------|
| Time: 1¼ Hours Roll No. (in figures) | Max. Marks : 100 (in words) | Total Questions: 100 |
| Name | | |
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| 1. | 1. A Pseudo code is a | Proceeding to the law as a |
|-----|--|--|
| | (1) Program (2) Algorithm | (3) Can't say (4) None |
| 2. | SCSI is: (1) Small Computer System Interface (2) Small Computer System Interchange (3) Simple Computer Secondary Information (4) Simple Computer Secondary Interface | ition |
| 3. | (1) Basic Input Output System (| Begin Input Output System Base Instruction Output System |
| 4. | (1) RAM chips (2) | pe volatile ? 2) ROM chips 4) None of the above |
| 5. | . Which of the following memories has the | shortest access times? |
| | (1) (2 1 | 2) Magnetic core memory |
| | (3) RAM . (4 | 4) PROM |
| 6. | . According to Niklaus Wirth, a computer s | cientist, program consist of : |
| | (1) 41 | 2) Algorithm only |
| | (3) Data only | 1) None of the above |
| 7. | Graphical representation of control flow in | n a program can be depicted through: |
| | (1) Gantt chart | |
| | (3) Flow chart (4 | None of the above |
| 8. | In flow chart, diamond box is used for: | |
| | (1) D | Data input/output |
| | (3) Calculation and data manipulation (4) | As connector |
| 9. | Which Loop is called as Entry Control Loo | p? |
| | |) Both (4) None |
| 10. | strlwr () is used for: | ended to an extensión y and about 12.1 |
| | (1) Converting string into Integer (2 | Converting string into Lower Case |
| | (3) Converting string into Upper Case (4) | |
| | | |

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| 11. | "%s" is used for specifying: (1) Signed Variable (3) String | | Short Variable All of the above | 9 | | |
|-----|--|--------------|---|------|-------------|---------|
| 12. | free () is a: (1) Memory Allocation Function (3) Memory De-Allocation Function | | Recursive Func None | tion | | |
| 13. | Cgets () and Cputs () are defined in: (1) stdio,h (2) conio.h | (3) | math.h | (4) | All of the | e above |
| 14. | To get a character from unformatted I/((1) scanf() (2) getche() | | | | | |
| 15. | Write the Output for: main() { int i = 5, j = 6; printf ("% d", 0 && (i = 10)); | | | | | |
| | (1) 0 (2) 1 | (3) | 10 | (4) | None | |
| 16. | <pre>Write the output of: main() { int n 1 = 4; printf ("%d", size of (n1)); } (1) 2 (3) 1</pre> | | 4 | | | |
| 17. | Which of the following operator can no (1) Scope resolution operator (3) Equality operator | (2) | overloaded ? Arrow operator Assignment op | | or . | |
| 18. | How do we declare an interface class? (1) By declaring the class as interface w (2) By making all the methods abstract (3) By making all the methods pure vir (4) It is not possible to create interface. | usir tual | ng the keyword a in class | | act in clas | ss . |

| 19. | (1) X (const X* arg) (3) X (X arg) | | nst X & arg) | , |
|------|---|--|--|----------|
| 20. | Which statement is most appropriate for (1) Type less language (3) Dynamically typed language | | ally types language | |
| 21. | Among the logic families, the familier greater than 100 MHz in a 4 bit synchr | | | requency |
| | (1) TTLAS (2) CMOS | (3) ECL | (4) TTLLS | |
| 22. | An AND gate will function as OR if: (1) all the inputs to the gates are "1" (2) all the inputs are '0' (3) either of the inputs is "1" (4) all the inputs and outputs are com | plemented | | |
| 23. | An OR gate has 6 inputs. The number | of input wo | rds in its truth table are: | |
| | (1) 6 (2) 32 | (3) 64 | (4) 128 | |
| 24. | NAND gates are preferred over others (1) have lower fabrication area (3) consume least electronic power | (2) can b | ese: e used to make any gate de maximum density in a c | hip |
| 25. | In case of OR gate, no matter what the (1) 1 at any input causes the output to (2) 1 at any input causes the output to (3) 0 any input causes the output to b (4) 0 at any input causes the output to | be at logic be at logic e at logic 0 | | |
| 26. | The most common addressing techniq | ues employe | ed by a CPU is: | |
| | (1) immediate (2) direct | (3) indire | ect (4) all of the about | ove . |
| 27. | Pipeline implement: (1) fetch instruction and decode instruction (2) fetch operand and calculate opera (3) execute instruction (4) all of the above | | sclare and some of a some of a some class some class some class some class some some some of a s | |
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| 28. | corporation? | | esent day computing was developed by IBM |
|-----|---|-------|--|
| | (1) ASCII | (2 | 2) Hollerith Code |
| | (3) Baudot code leixe | (4) | 4) EBCDIC code |
| 29. | When a subroutine is called, the ad instructions stored in/on the: | dres | ess of the instruction following the CALL |
| | (1) Stack pointer | (2) | 2) Accumulator |
| | (3) Program counter | (4) | 4) Stack |
| 30. | A microprogram written as string of 0's | and | nd 1's is a : |
| | (1) Symbolic microinstruction . | | |
| | (3) Symbolic microprogram | | 1) Binary microprogram |
| 31. | If a, b, c are different and: | | |
| | $\begin{vmatrix} 0 & x-a & x-b \\ x+a & 0 & x-c \\ x+b & x+c & 0 \end{vmatrix} = 0, \text{ then } x \text{ is equal}$ | to | |
| | (1) a (2) b | (3) | 3) c (4) 0 |
| 32. | If A is $\begin{bmatrix} 8 & -6 & 2 \\ -6 & 7 & -4 \\ 2 & -4 & \lambda \end{bmatrix}$ is a singular matr | ix th | then $\lambda =$ |
| | (1) 3 | | 2) 4 |
| | (3) 2 | | y 5 The continuous sign Alond and other and |
| | (6) 2 | (+) | August of the control of the August A |
| | [3 -3 4] | | and the self-results of the first of the self-results of the |
| 33. | If $A = \begin{bmatrix} 2 & -3 & 4 \\ 0 & -1 & 1 \end{bmatrix}$, then $A^{-1} = \begin{bmatrix} 1 & 1 & 1 \\ 0 & -1 & 1 \end{bmatrix}$ | | OR IT SUPPLY TO SUPPLY CONTROL THE OUTE OF THE |
| | | | |
| * | (1) A steed (2) A ² | (3) |) A^3 (4) A^4 |
| 34. | If $A_{\alpha} = \begin{bmatrix} \cos \alpha & \sin \alpha \\ -\sin \alpha & \cos \alpha \end{bmatrix}$, then $A_{\alpha} A_{\beta}$ is ϵ | equa | al to: |
| | (1) $A_{\alpha+\beta}$ (4) (2) $A_{\alpha\beta}$ | (3) |) $A_{\alpha-\beta}$ (4) none of these |
| 35. | If A and B are symmetric matrices, then | AB | B – BA is a : |
| | (1) Symmetric matrix | |) Skew symmetric matrix |
| | (3) Diagonal matrix | | |
| | | 1 | |

| 36. | The inverse of a skew symmetric matri | ix of odd order is: wollo | |
|-----|--|--|------|
| | (1) a symmetric matrix | (2) a skew symmetric matrix | |
| | (3) diagonal matrix | (4) does not exist | |
| 37. | The value of λ for which the system of | | |
| | $2x-y-2x=2$ $x-2y+z=-4$ $x+y+\lambda z=4$ has no solution, is (1) -3 (2) 3 | (3) 2 (4) -2 | |
| 38. | If $A = \begin{bmatrix} \alpha & 0 \\ 1 & 1 \end{bmatrix}$ and $B = \begin{bmatrix} 1 & 0 \\ 5 & 1 \end{bmatrix}$, then the | e value of α for which $A^2 = B$, is: | |
| | (1) 1 (2) -1 | (3) 4 (4) no real va | lues |
| 39. | 1 1 | 1 is positive, then: c (3) abc < -8 (4) abc > -2 | |
| 40. | The value of $\begin{vmatrix} a & a^2 - bc & 1 \\ b & b^2 - ca & 1 \\ c & c^2 - ab & 1 \end{vmatrix}$ is | | |
| | (1) 1 (2) -1 | (3) 0 (4) -abc | |
| 41. | is used to build 'intuitive' | | |
| | (1) SQL (2) Xbase | (3) QBE (4) XML | |
| 42. | | | |
| | | (3) query (4) macro | |
| 43. | To store your picture in a database re (1) BLOB (2) text | (3) image (4) logical | |
| 44. | . The is used to access one re | ecord at a time : Ommya | |

(1) query

(2) filter

(3) form

ximam (4) report

| 45. | 45. A transparent DBMS: (1) Can not hide sensitive information from users (2) Keep its logical structure hidden from users (3) Keeps its physical structure hidden from users (4) Both (2) and (3) | |
|-----|--|--------------------------------------|
| 46. | 46. An unnormalized relation contains values : | |
| | (1) Atomic (2) Non-A | tomic |
| | (3) Classified (4) None of | of these |
| 47. | 47. A relation scheme is said to be in form if attribute of the relation are atomic. | the values in the domain of each |
| | (1) Unnormalized (2) First N | ormal |
| | (3) Boyce CODD (4) None o | of these |
| 48. | 48. A second normal form does not permitd attribute and the relation key. | dependency between a non prime |
| | (1) Partial (2) Multi (3) Function | onal (4) Valued |
| 49. | 49. A relation scheme is inif it is in the 1NF and if functionally dependent on the relation key . | f all non prime attributes are fully |
| | | Normal Form |
| | (3) Boyce CODD Normal Form (4) Fourth | Normal Form |
| 50. | 50. In a Third Normal Form relation, everyattri dependent on the every candidate key. | bute is non-transitively and fully |
| | (1) Prime (2) Non prime (3) Unique | (4) None of these |
| 51. | 51. The collection of communication lines and routers is | called: |
| | (1) LAN (2) MAN | |
| | (3) WAN (4) Commu | unication Subnet |
| 52. | 52. In the IEEE standards 802.5 standard is also called | |
| | (1) Ethernet (2) Token E | Bus |
| | (3) Wireless Token Area Network (4) Token F | Ring |
| 53. | 53 have a single communication channel that network. | t is shared by all the users on the |
| | (1) Point-to-Point (2) Broadca | ast network |
| | (3) Protocol (4) PAN | |
| | | |

| 54. | Error detection at the data link level is | achie | eved by |
|-----|---|--------|---|
| | (1) Bit stuffing | (2) | Hamming codes |
| | (3) Cyclic Redundancy codes | (4) | Equalization |
| 55. | Which of the following is an advantage | to u | sing fibre optics data transmission? |
| | (1) Resistance to data theft | | Fast data transmission rate |
| | (3) Low noise level | (4) | All of the above |
| 56. | Which of the following is required to co | mm | unicate between two computers? |
| | (1) Communication software | | |
| | (2) Protocol | | |
| | (3) Communication hardware | | • |
| | (4) All of the above including access to | tran | smission medium |
| 57. | Which of the following types of channe | ls m | oves data relatively slowly? |
| | (1) Wide band channel | (2) | Voice band channel |
| | (3) Narrow band channel | (4) | All of the above |
| 58. | A protocol is a set of rules governing a | time | sequence of events that must take place: |
| | (1) between peers | (2) | between an interface |
| | (3) between modems | (4) | across an interface |
| 59. | Which of the following transmission individual device? | syst | ems provide the highest data rate to in |
| | (1) Computer bus | (2) | Telephone lines |
| | (3) Voice and mode | (4) | Lease lines |
| 60. | Communication circuits that transmit of are operating in: | lata i | in both directions but not at the same time |
| | (1) a simplex mode | (2) | a half duplex mode |
| | (3) a full duplex mode | (4) | an asynchronous mode |
| 61. | page replacement algorithm su | affer | s from Belady's anamoly. |
| | (1) LRU | (2) | MRU |
| * | (3) FIFO | (4) | LIFO . |
| 62. | is a high speed cache used to part of paged virtual memory. | o hol | d recently referenced page table entries a |
| | (1) Translation lookaside buffer | (2) | Inverse page table |
| | (3) Segmented page table | (4) | All of the above |

| | (1) Long term scheduler (2) Snort term scheduler |
|------|---|
| | (3) Medium term scheduler (4) Dispatcher |
| 64. | In interactive environments such as time-sharing systems, the primary requirement is to provide reasonably good response time and in general, to share system resources equitably. In such situations, the scheduling algorithm that is most popularly applied is |
| | (1) Shortest Remaining Time Next (SRTN) Scheduling |
| | (2) Priority Based Preemptive Scheduling |
| | (3) Round Robin Scheduling |
| * | (4) None of the above |
| 65. | In the multi-programming environment, the main memory consisting of |
| | (1) Greater than 100 (2) Only one (3) Greater than 50 (4) More than one |
| | (4) More than one |
| 66. | In a multithreaded environment |
| | (1) Each thread is allocated with new memory from main memory |
| | (2) Main thread terminates after the termination of child threads |
| | (3) Every process can have only one thread |
| | (4) None of the above |
| 67. | Which of the following statement is not true? |
| | (1) Multiprogramming implies multitasking |
| | (2) Multi-user does not imply multiprocessing |
| | (3) Multitasking does not imply multiprocessing |
| | (4) Multithreading implies multi-user |
| 68. | In one of the deadlock prevention methods, impose a total ordering of all resource types, and require that each process requests resources in an increasing order of enumeration. This violates the condition of deadlock. |
| | (1) Mutual exclusion (2) Hold and wait |
| | (3) Circular wait (4) No preemption |
| 69. | In themethod of data transfer, the participation of the processor is eliminated during data transfer. |
| | (1) Buffering (2) Caching |
| | (3) Direct Memory Access (4) Indirect Memory Access |
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63. does the job of allocating a process to the processor :

| 70. | A thread is aprocess. | | |
|-----|--|------|---|
| | (1) Heavy weight | (2) | Multiprocess |
| | (3) Inter Thread | (4) | Light weight |
| 71. | Which of the following is not viewed as process? | saj | orimary mover in improving the software |
| | | (2) | Better product quality |
| | (3) Improved Staff Satisfaction | | Tighter managerial control |
| 72. | Symptoms of the software crisis would i | ncli | ade: www.eren.com/eren.com/eren.com/eren.com/eren.com/eren.com/eren.com/eren.com/eren.com/eren.com/eren.com/ere |
| | (1) Software delivered behind schedule | (2) | Software exceeding cost estimate |
| | (3) Unreliable | (4) | All of the above |
| 73. | Which of the following projects would paradigm for software development? | be | a good one for adopting the prototyping |
| | (1) Accounting system | (2) | Spreadsheet |
| | (3) Automobile cruise control | (4) | Algebra tutor |
| 74. | Views of quality software would not inc | lud | e: of down area. The half to confir 1.21 |
| | (1) Optimizing price and performance | | |
| | (2) Minimizing the execution errors | | |
| | (3) Conformance to specification | | Company of the property of the second |
| | (4) Establishing valid requirements | | |

- 75. Software configuration activities would not include:
 - (1) Identify change
 - (2) Control change
 - (3) Ensure improper implementation of change
 - (4) Report change to interested parties
- **76.** In planning a software project one would:
 - (1) Find ways to produce results using limited resources
 - (2) Pad the schedule to accommodate errors
 - (3) Overestimate the budget
 - (4) Structure the team to prevent administrative interference
- 77. A systematic approach to software development, as epitomized by the various life-cycle models, is useful in:
 - (1) Helping us understand the nature of the software product
 - (2) Convincing the customer that we know what we are doing
 - (3) Filling texts on software engineering
 - (4) Managing the various activities necessary to get the job done

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| 78. | A process view in software engineering | g woı | ald consider which of the following: |
|-----|--|--------|--|
| | (1) Product performance | (2) | Staffing |
| | (3) Functionality | (4) | Reliability |
| 79. | Software measurement is useful to: | | |
| | (1) Indicate quality of the product | (2) | Track progress |
| | (3) Assess productivity | (4) | All of the above |
| 80. | | durir | ng the management of a software project? |
| 00. | (1) Money | | Time |
| | (3) Product quality | (4) | Product quantity |
| 01 | What type of information should you | avoid | including on your website? |
| 81. | (1) Links to sites of interest | (2) | Private personal information |
| 4 | (3) Work and academic experience | | Graphical |
| 82. | website?(1) the web audience(2) the operating environment of you(3) the operating system of your visit(4) each consideration should determ | r isp | ald you know about before building your our web design choices |
| 83. | What is the language of the web? | | |
| | (1) Basic | . ` ' | C++ |
| | (3) MS Visual Basic | (4) | HTML |
| 84. | What does an HTML tag do? | | Care than a man programme of the |
| | (1) it specifies formatting and layout | instru | actions for your web page |
| | (2) it hides programming instruction | s fron | n view |
| | (3) it determines the organizational s | | |
| | (4) it connects your website to an ope | eratin | g environment |
| 85. | A web document is broken into sections? | tions. | What are the tags called that create these |
| | (1) Structure tags | (2 |) html tags |
| | (3) heading tags | (4 |) body tags |

| 86. | What should be the first and last pair of (1) |
|-----|---|
|-----|---|

| 95. | Which of the following is a non-linear data structure? | |
|------|--|--|
| | (1) Tree | (2) Array |
| | (3) Linked list | (4) None of the above |
| 96. | To implement requirement will | |
| 30. | (1) Stack | n of the following data structure is required? |
| | (3) Tree | (2) Queue |
| | (0) 1166 | (4) None of the above |
| 97. | Array passed as an argument t | o a function is interpreted as: |
| | (1) Address of the array | The same of the control of the contr |
| | (2) Values of the first elements | of the array |
| | (3) Address of the first elemen | |
| | (4) Number of element of the | |
| 98. | Time taken for addition of element in queue is: | |
| | (1) O (1) | (2) O (n) |
| | (3) O (log n) | (4) None of these options |
| 99. | The memory address of the first element of an array is called: | |
| | (1) Floor address | (2) Foundation address |
| | (3) First address | (4) Base address |
| 100. | The memory address of fifth element of an array can be calculated by the formula: | |
| | (1) LOC (Array [5]) = Base (Array) + w (5-lower bound), where w is the number of words per memory cell for the array | |
| | (2) LOC (Array [5]) = Base (Array [5]) + w (5-lower bound), where w is the number of words per memory cell for the array | |
| | (3) LOC (Array [5]) = Base (Array [4]) + w (5-Upper bound), where w is the number of words per memory cell for the array | |
| | (4) None of the above | |
| | | |